

FIG. 1

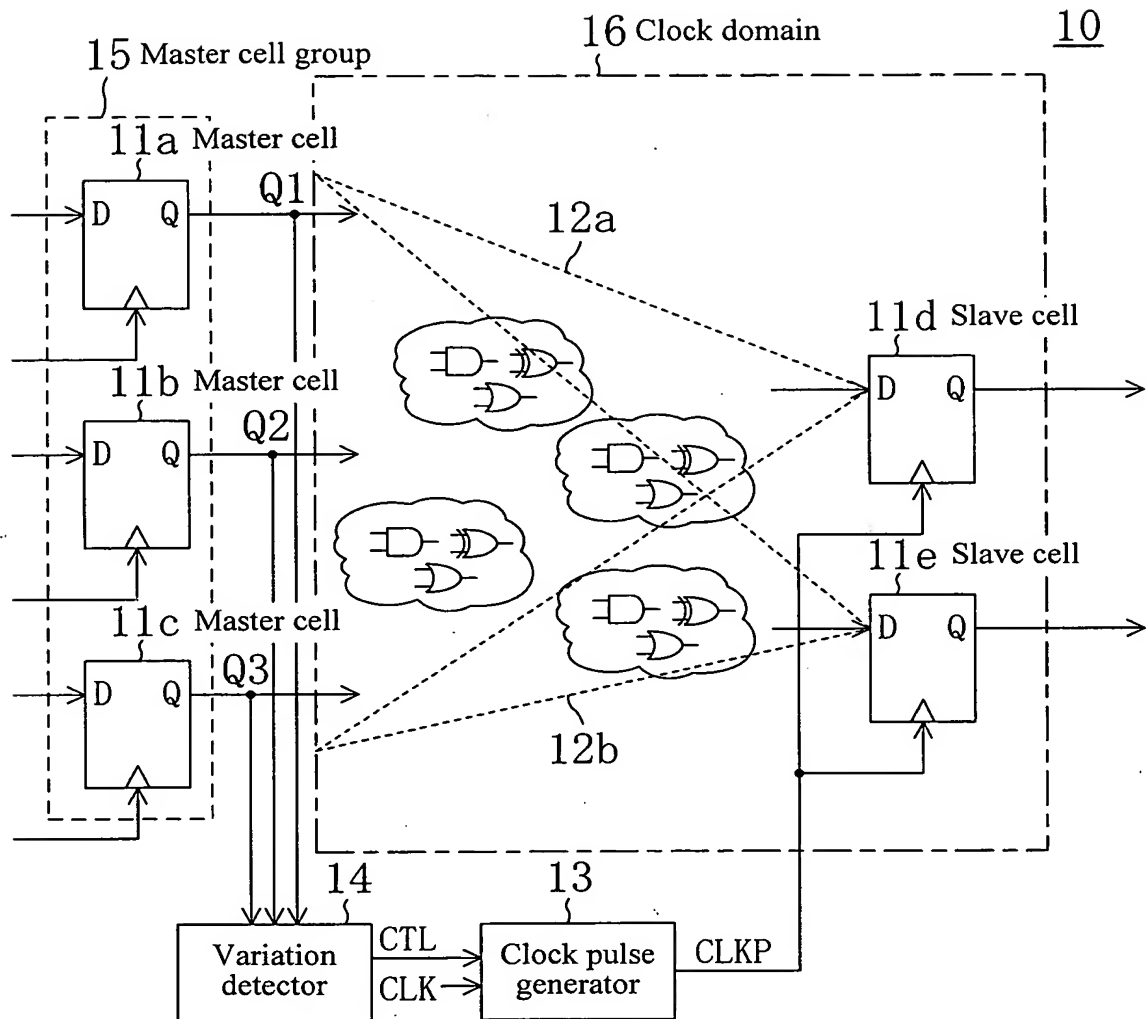


FIG. 2

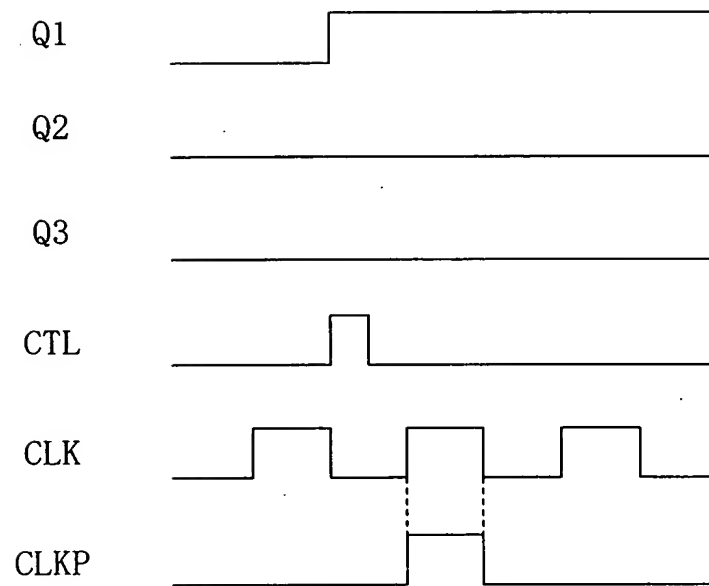


FIG. 3

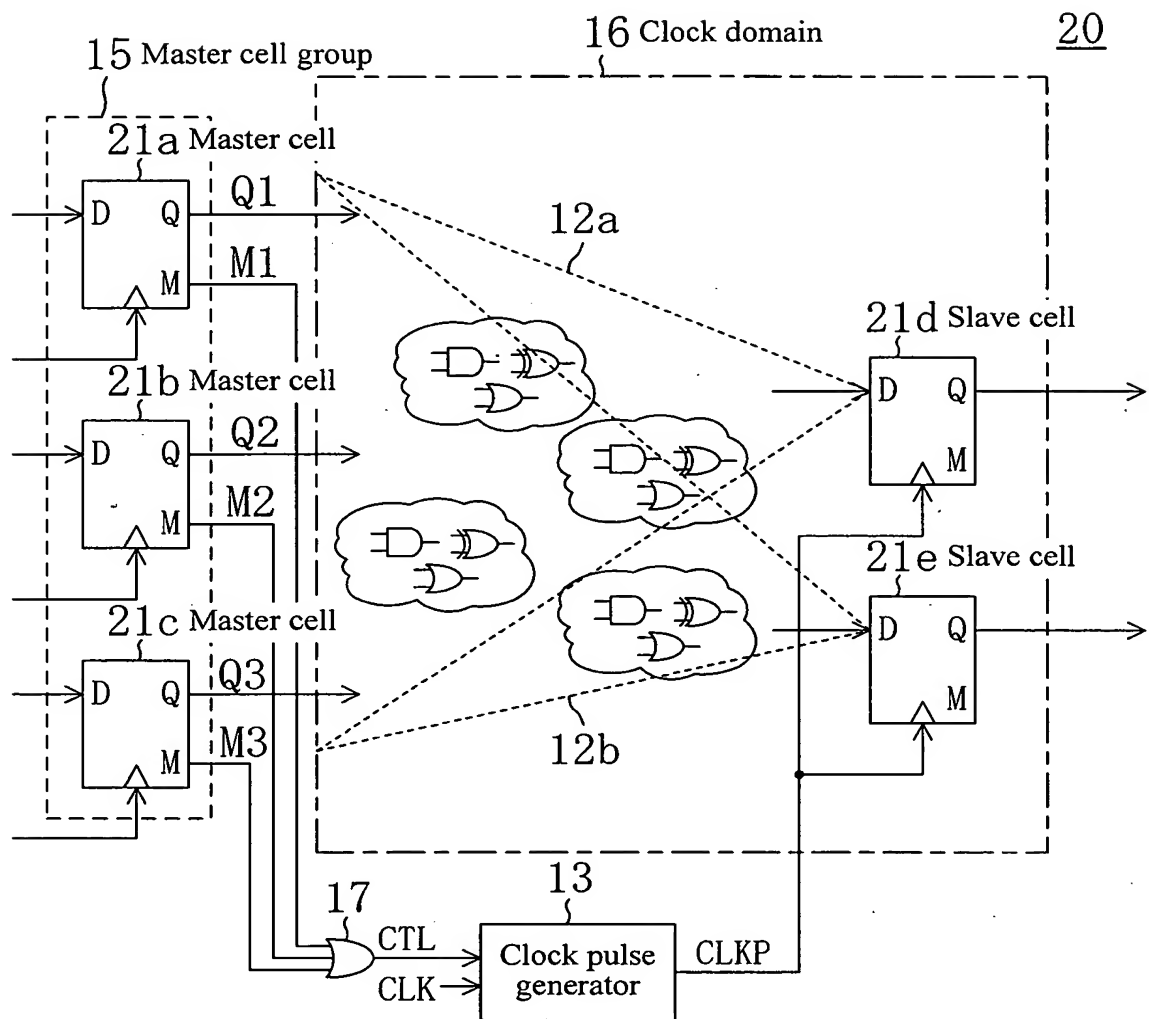


FIG. 4

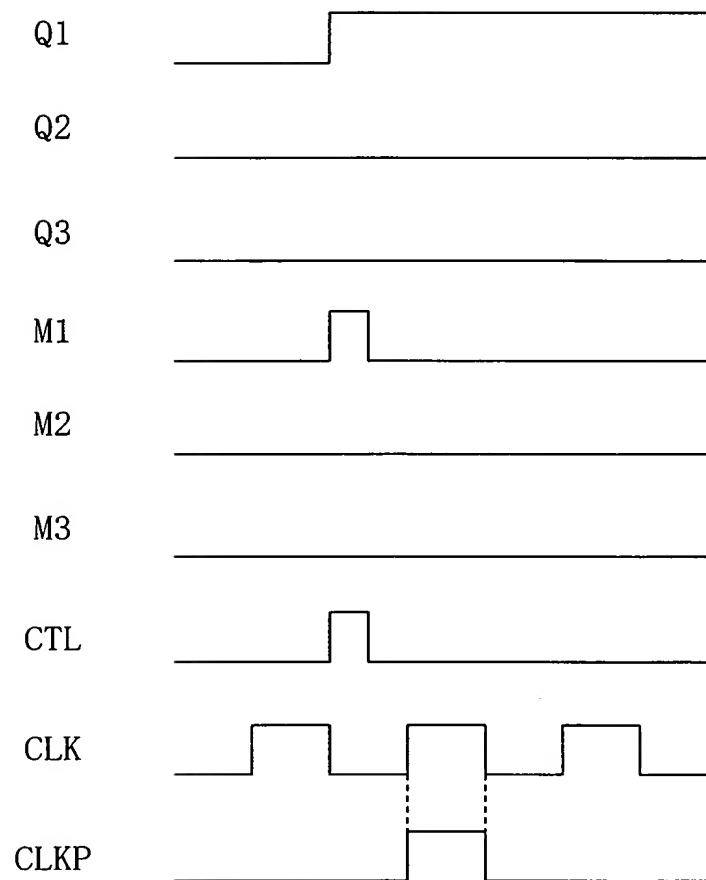


FIG. 5

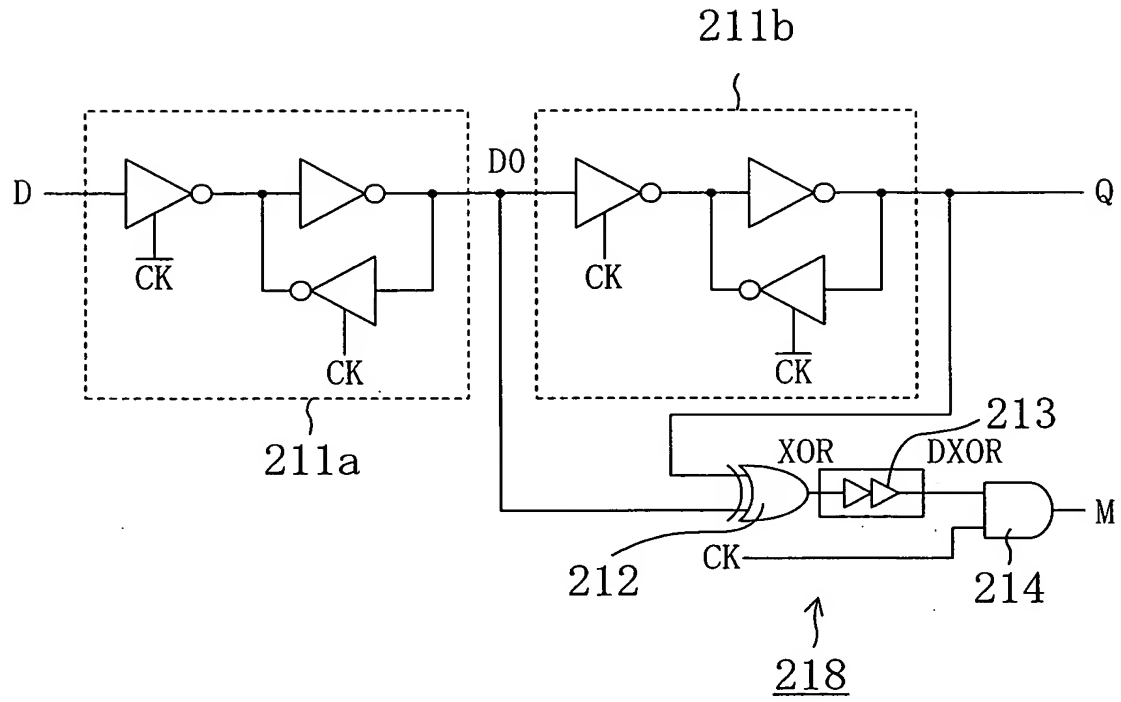
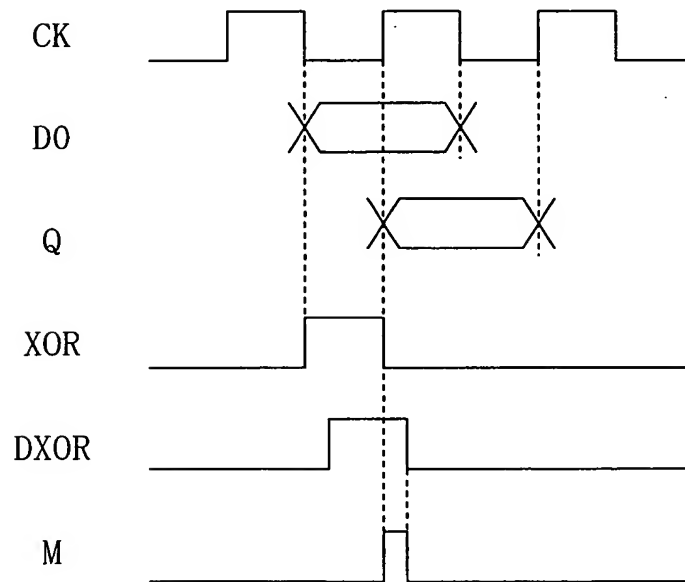
21A

FIG. 6



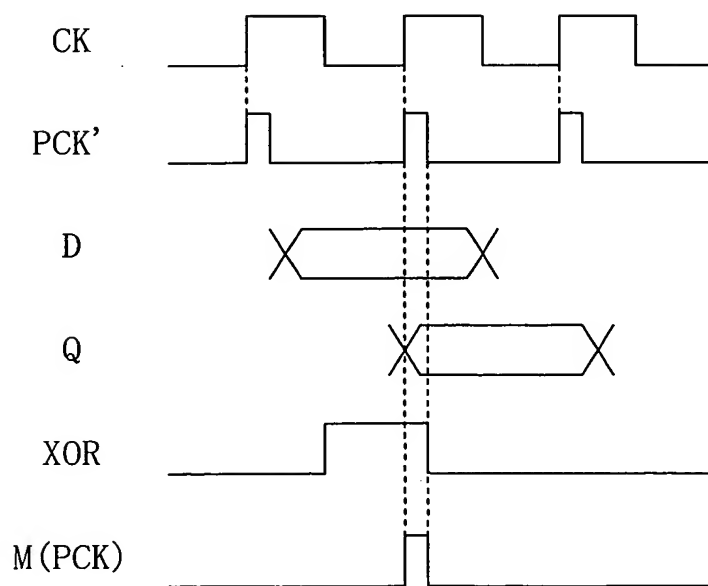


FIG. 9

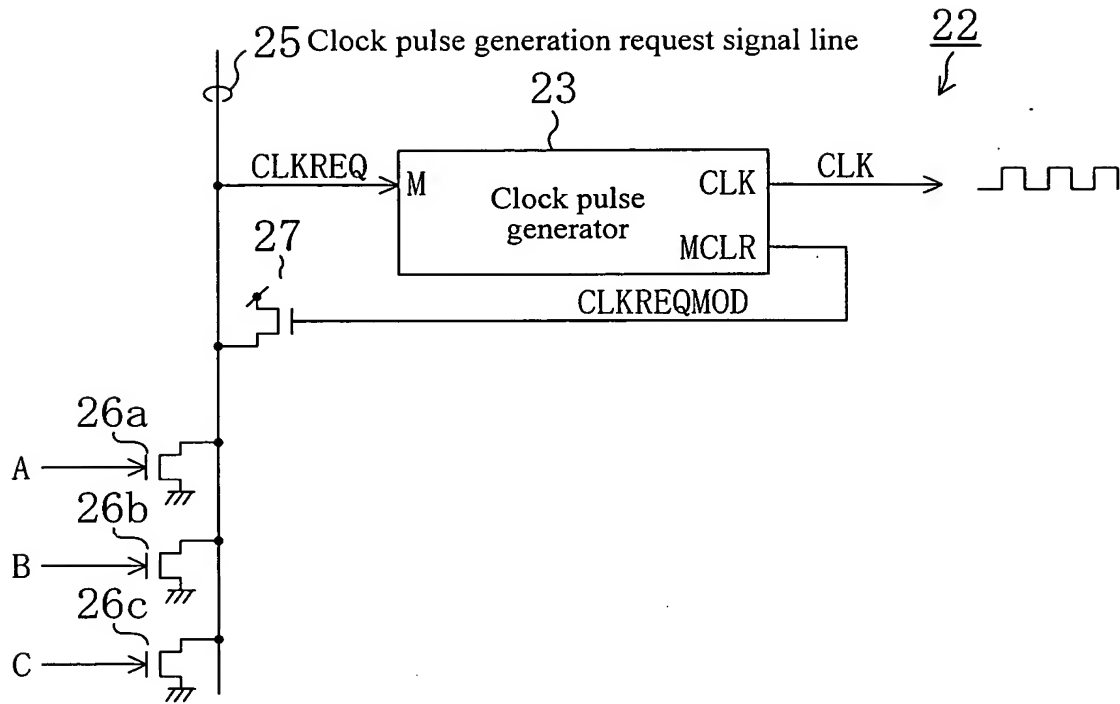


FIG. 10

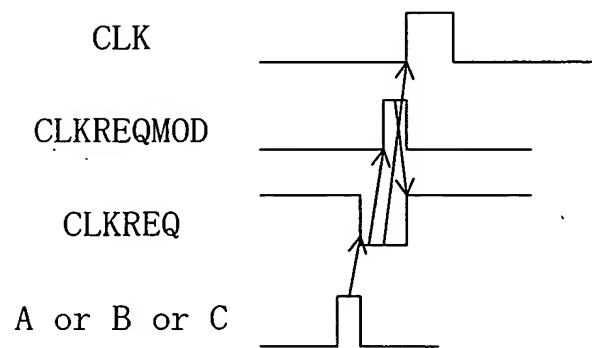


FIG. 11

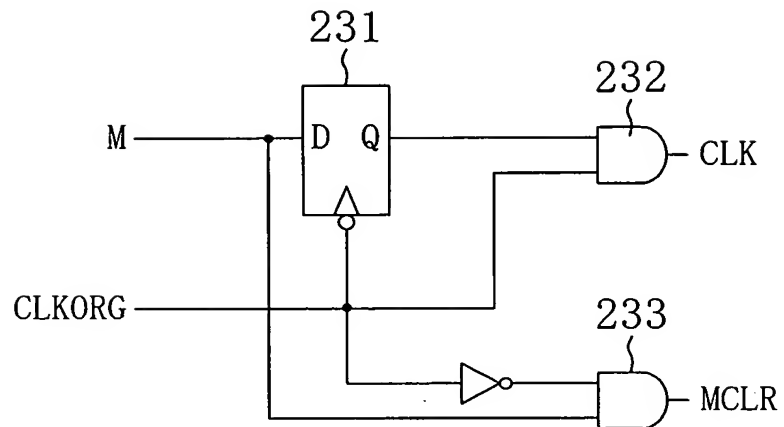
23A

FIG. 12

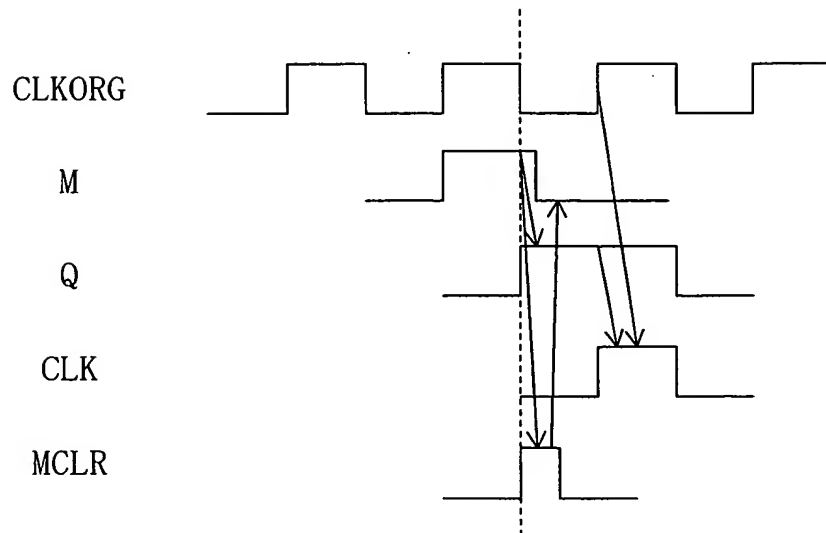


FIG. 13

23B

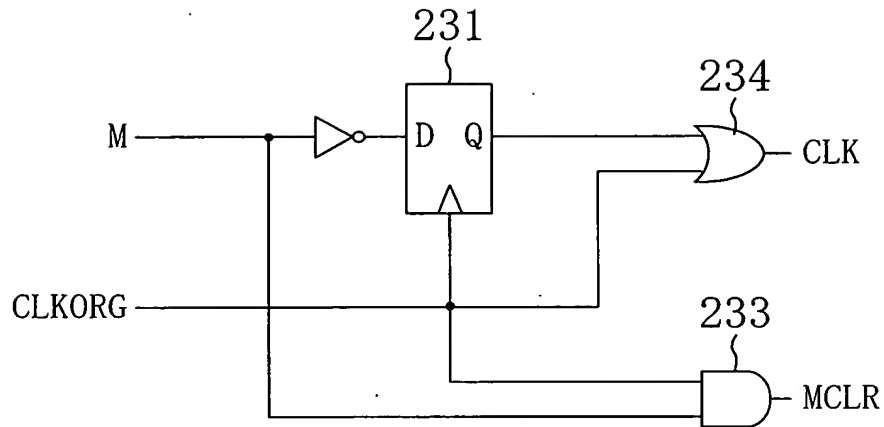


FIG. 14

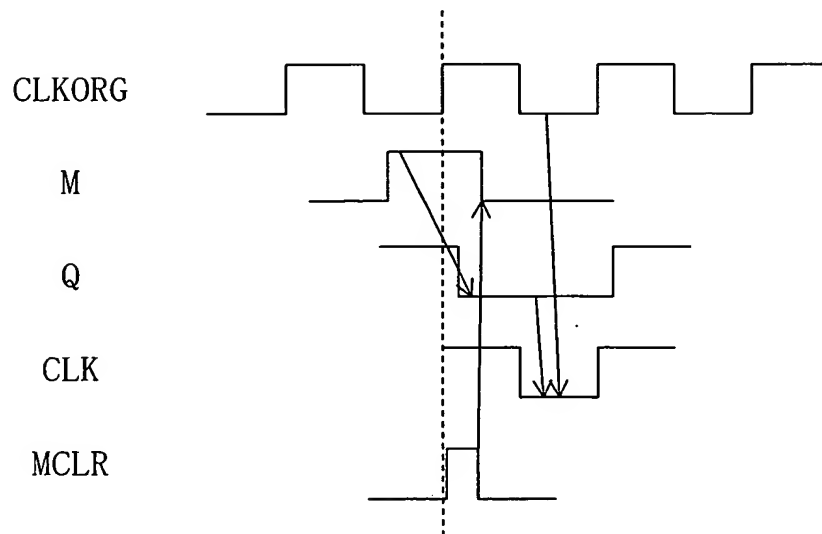


FIG. 15

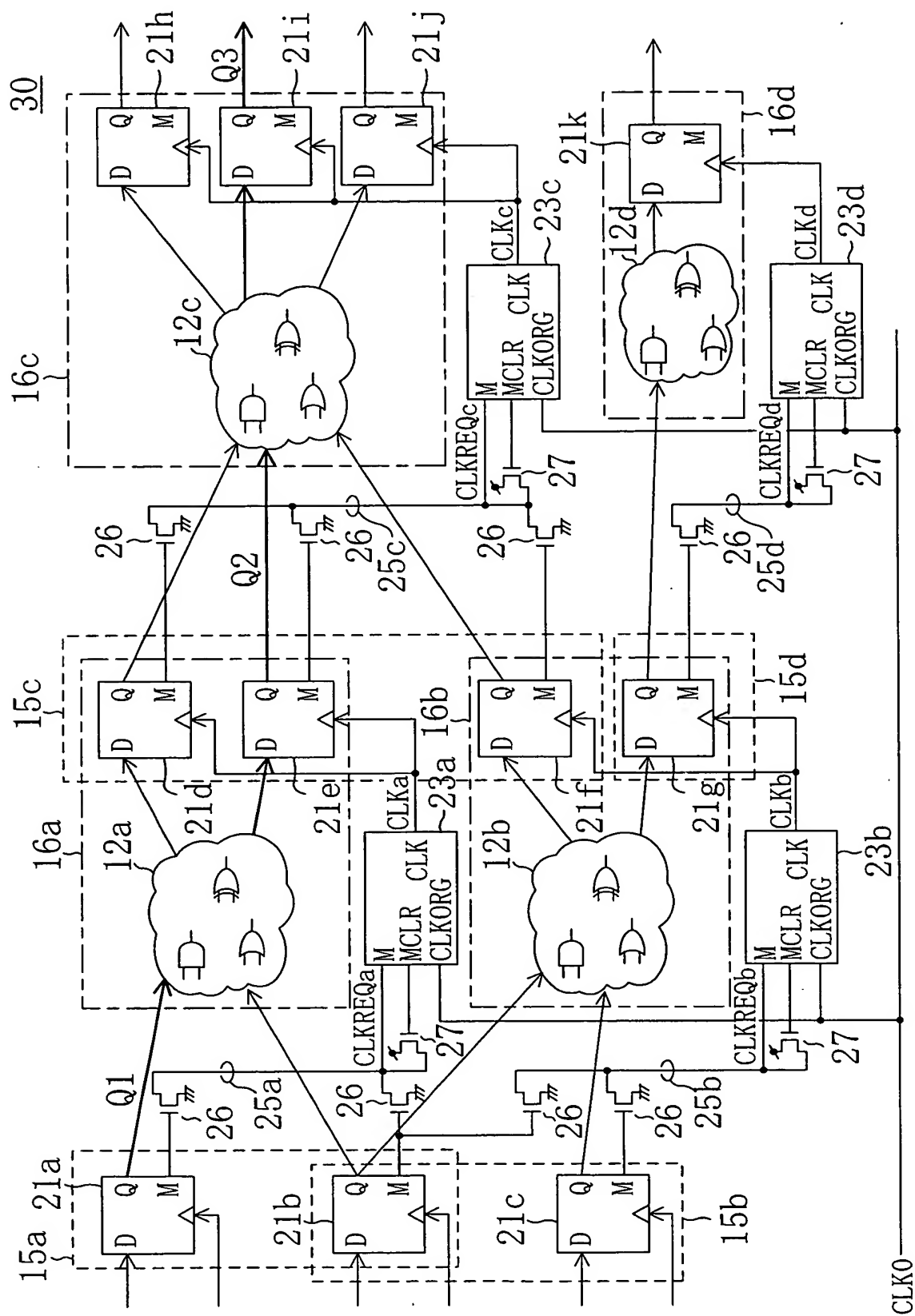


FIG. 16

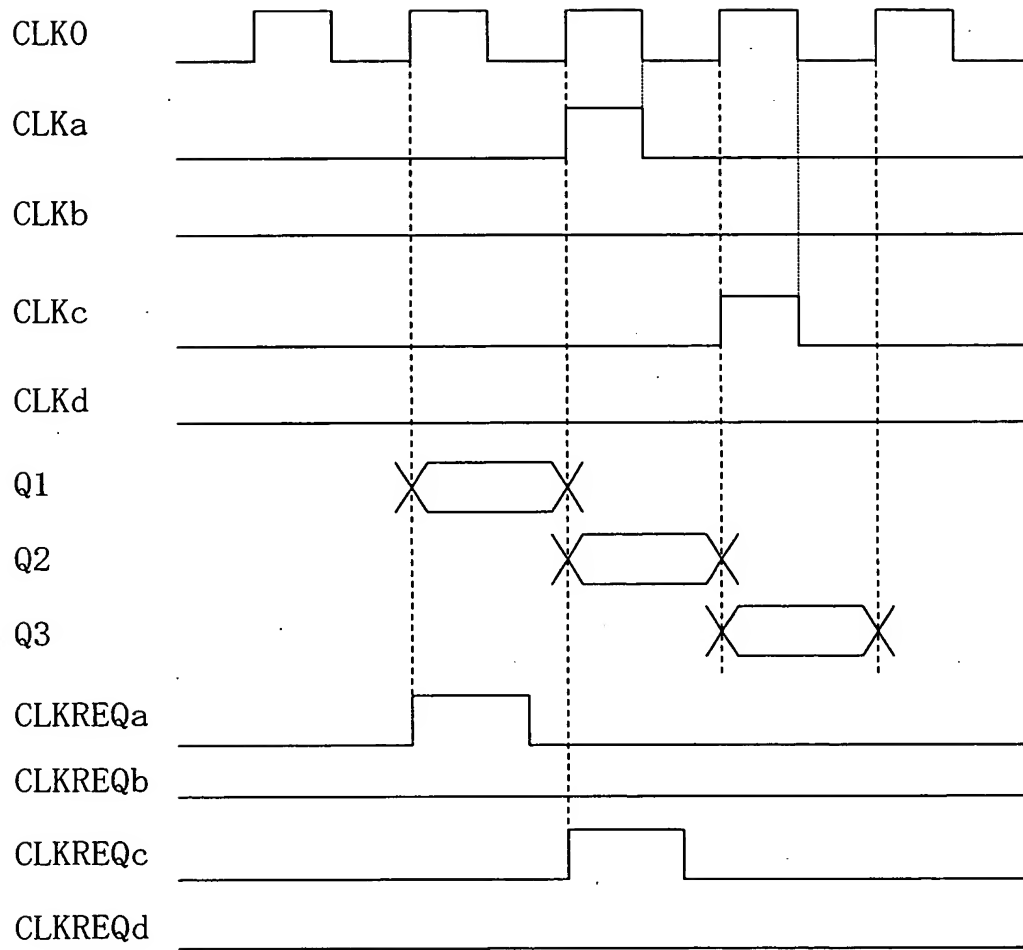
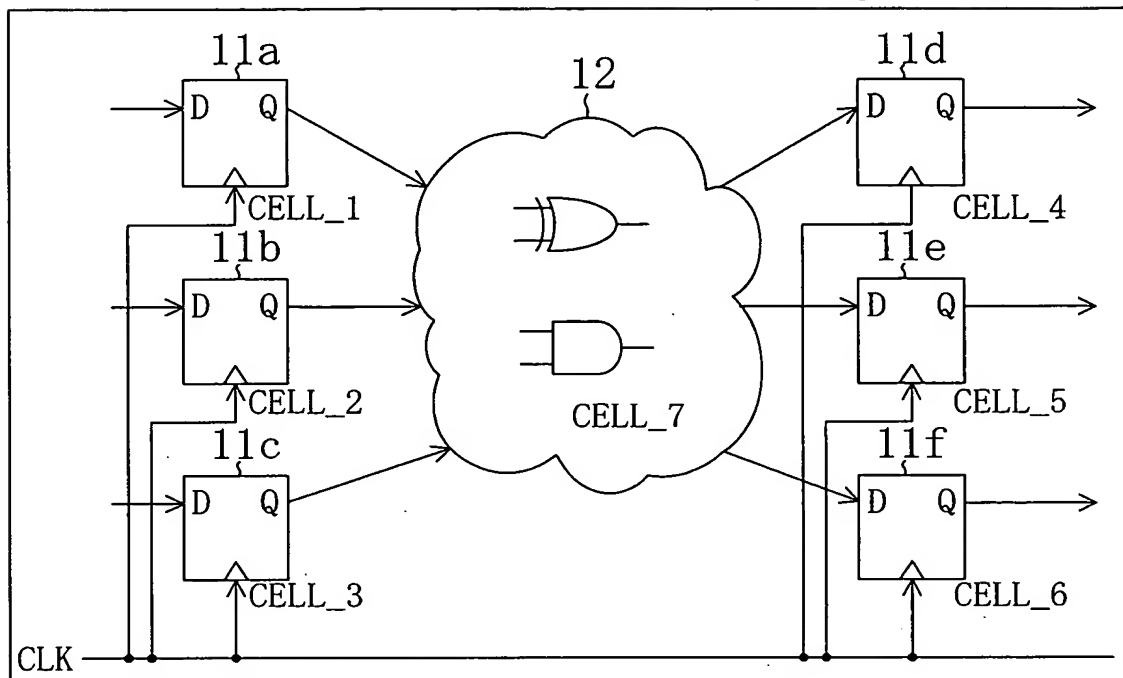
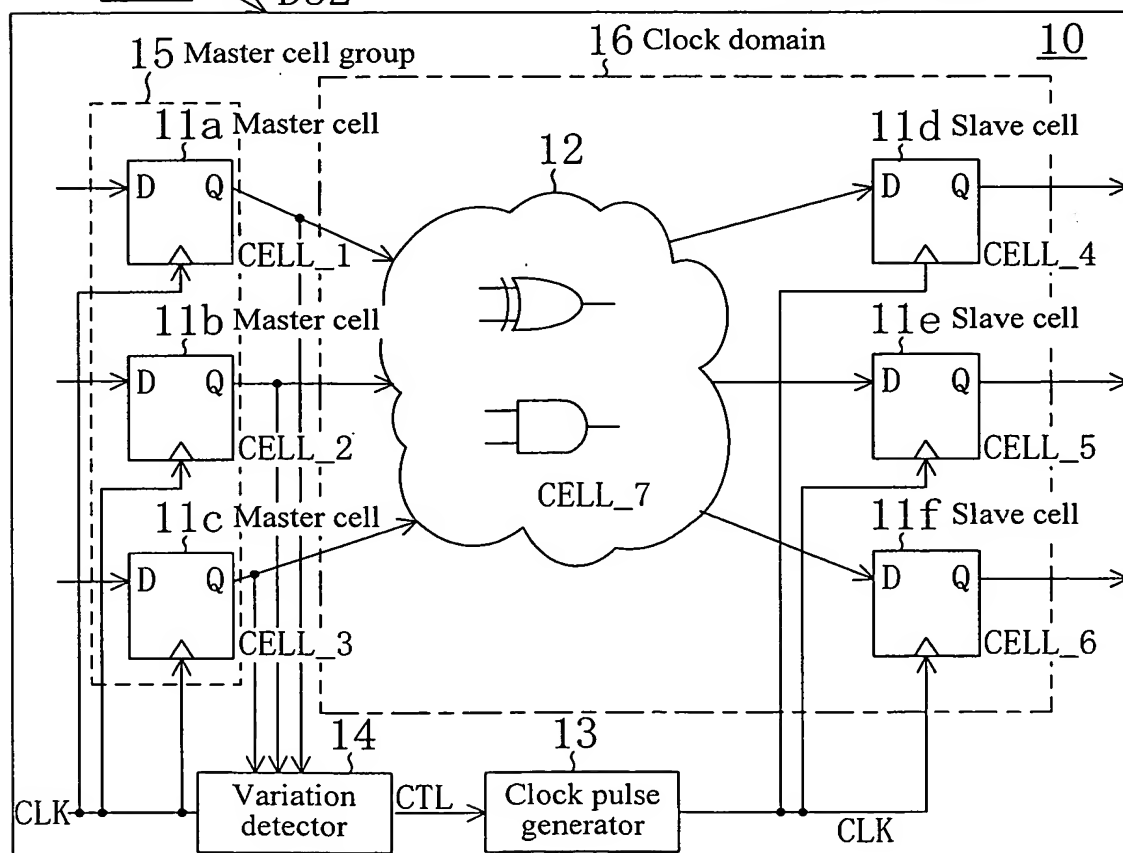


FIG. 17

Original sequential circuit



New sequential circuit



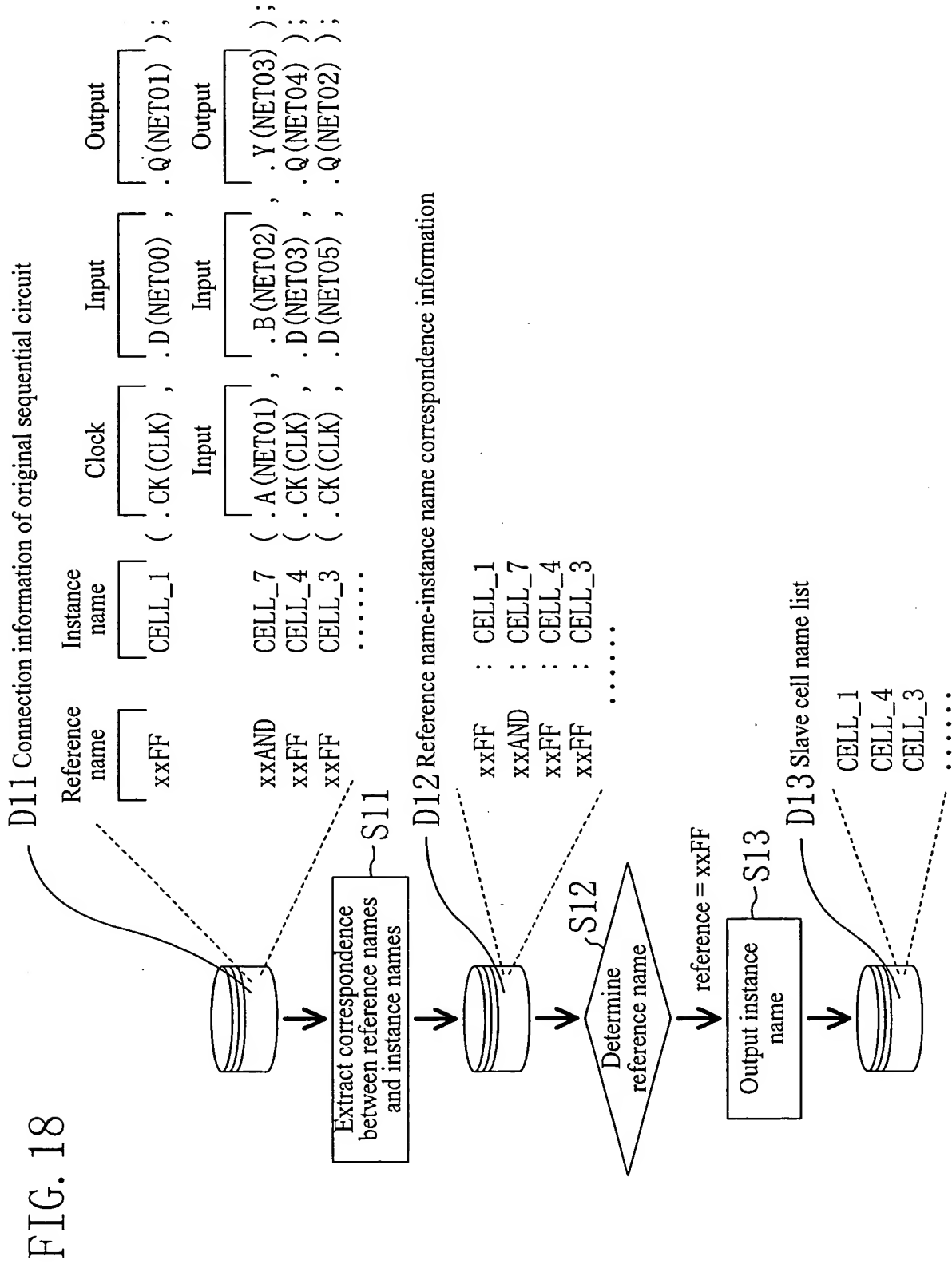


FIG. 19

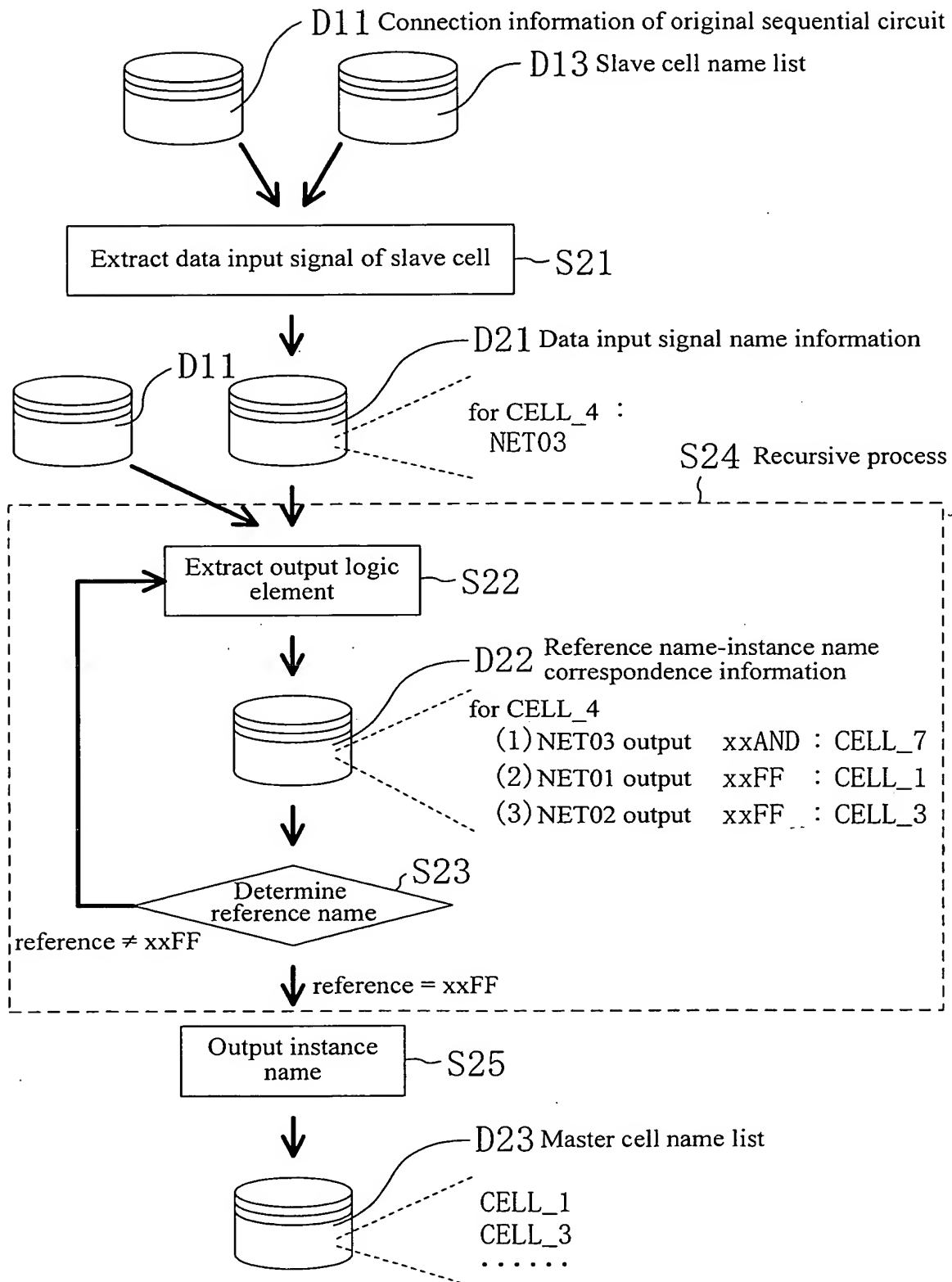


FIG. 20

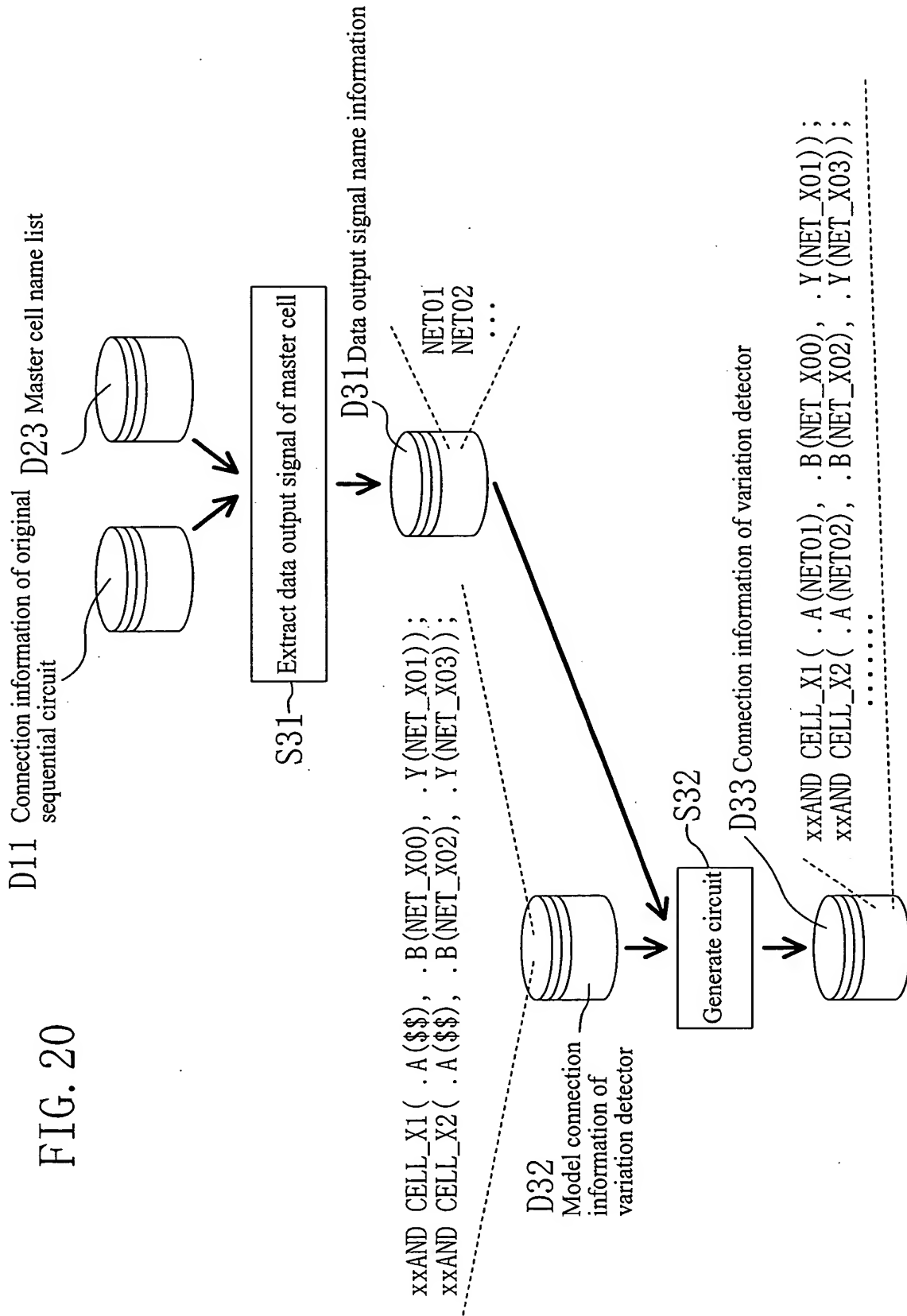


FIG. 21

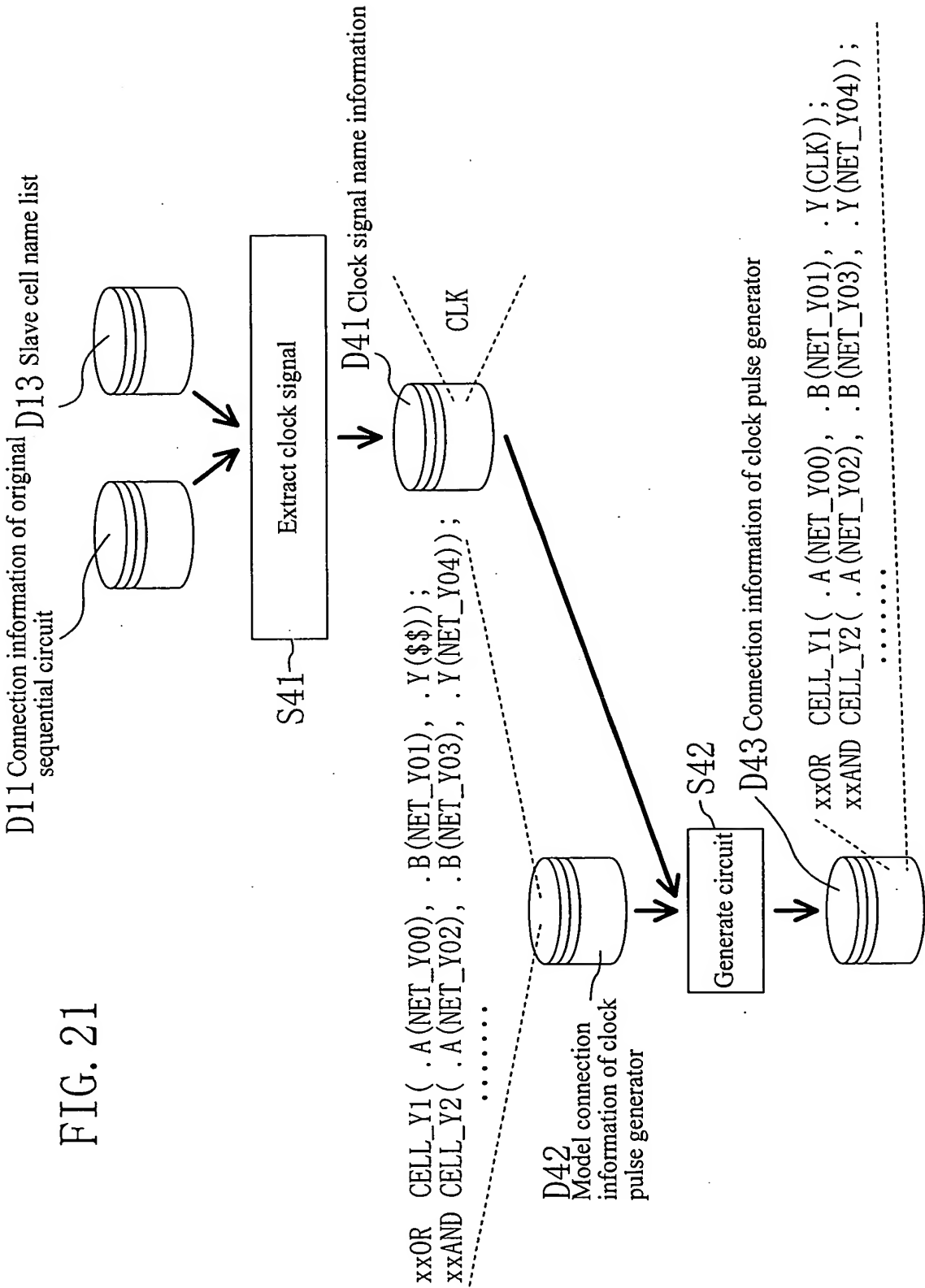


FIG. 22

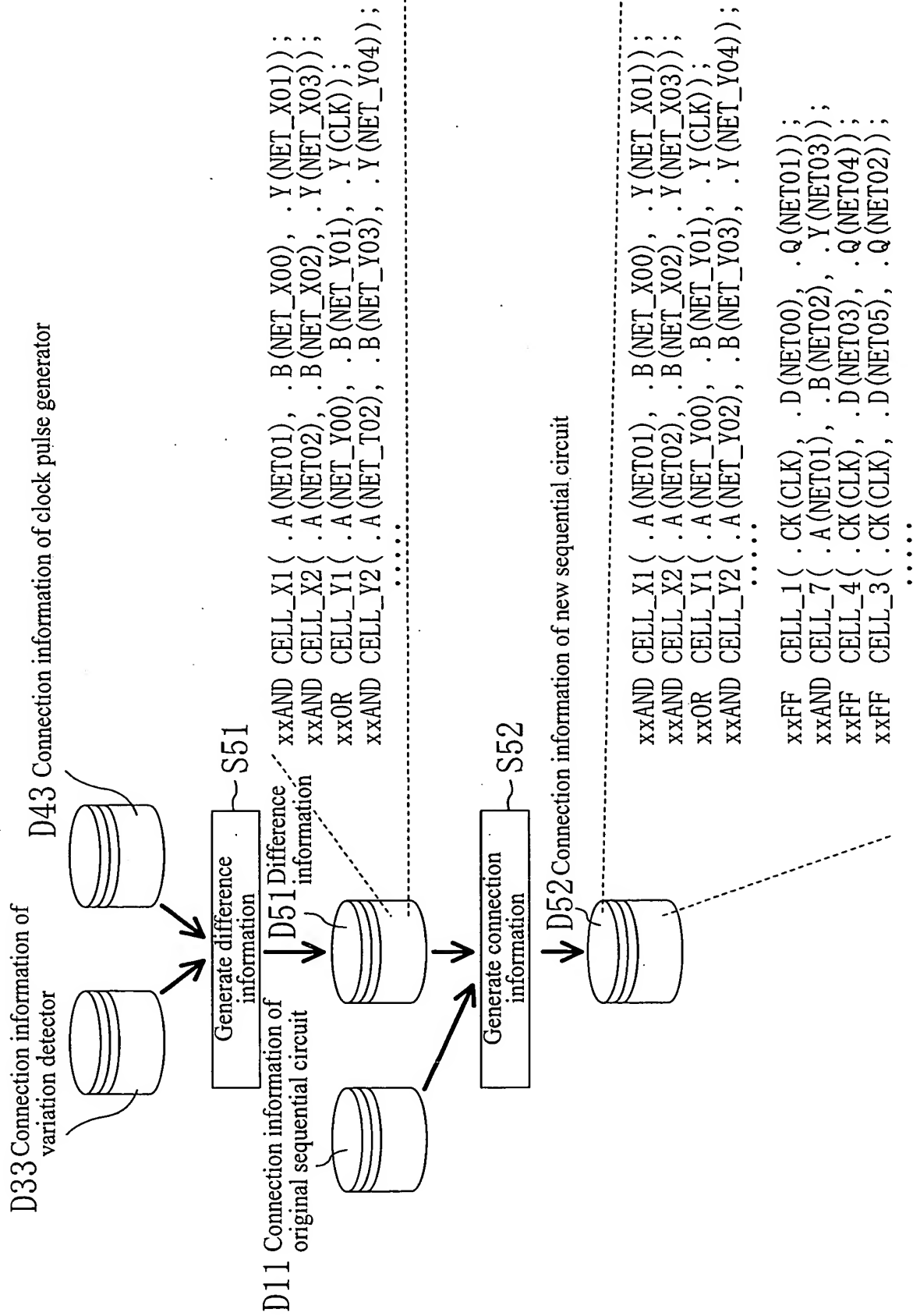
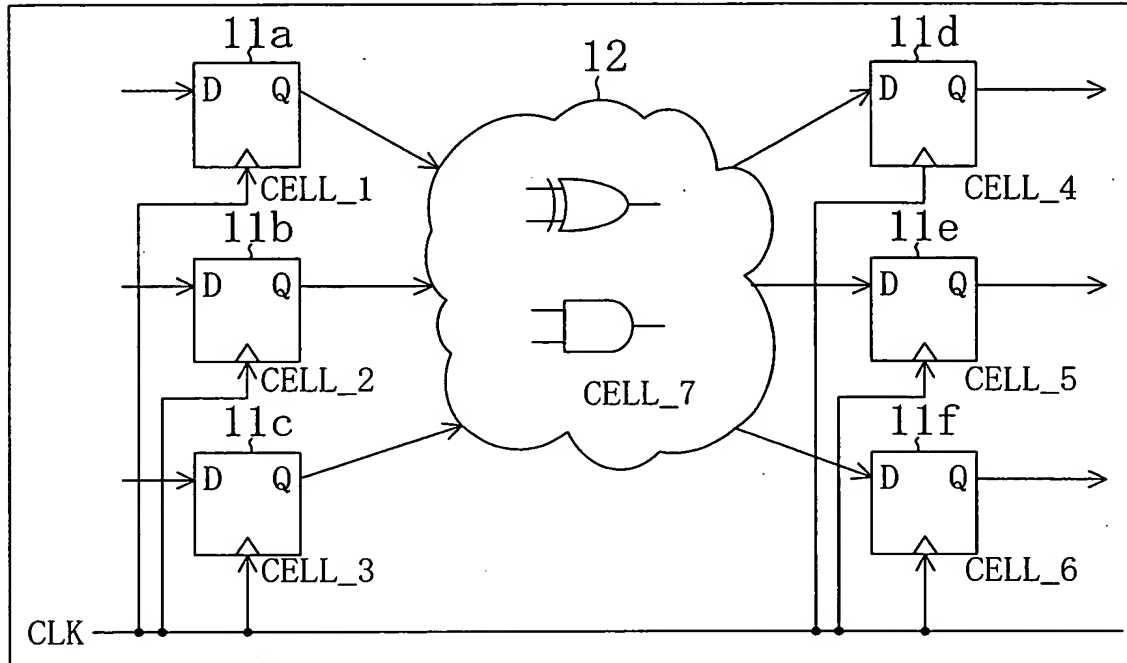


FIG. 23

Original sequential circuit



New sequential circuit

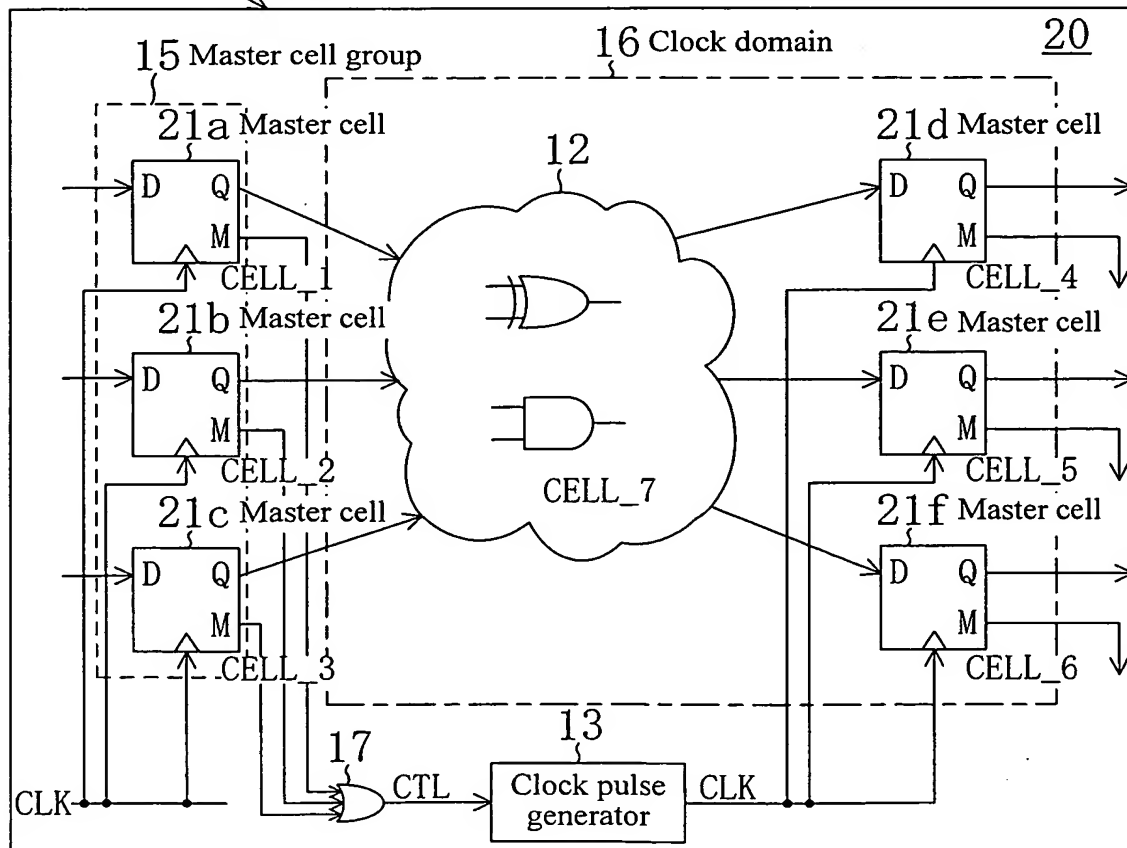


FIG. 24

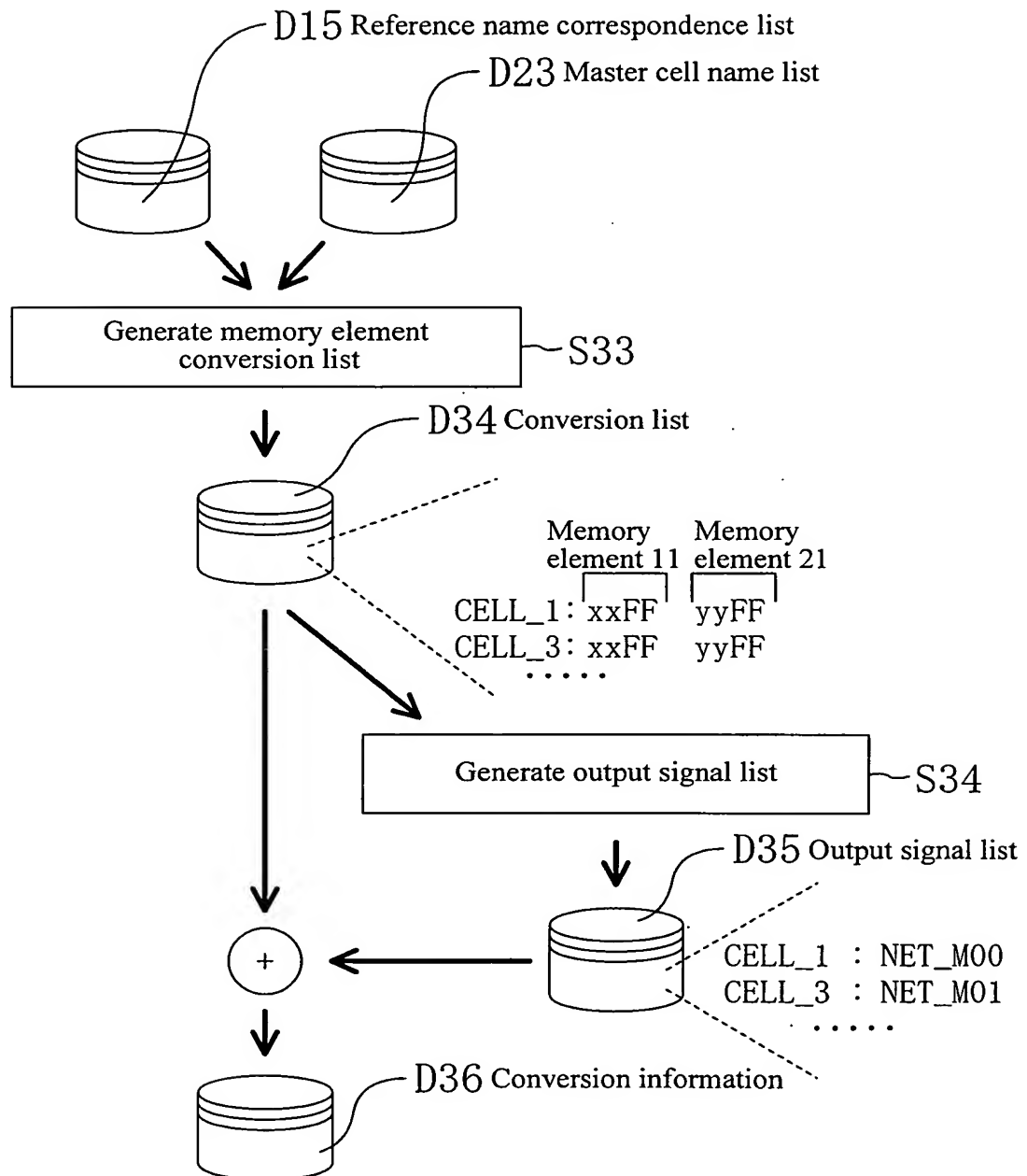


FIG. 25

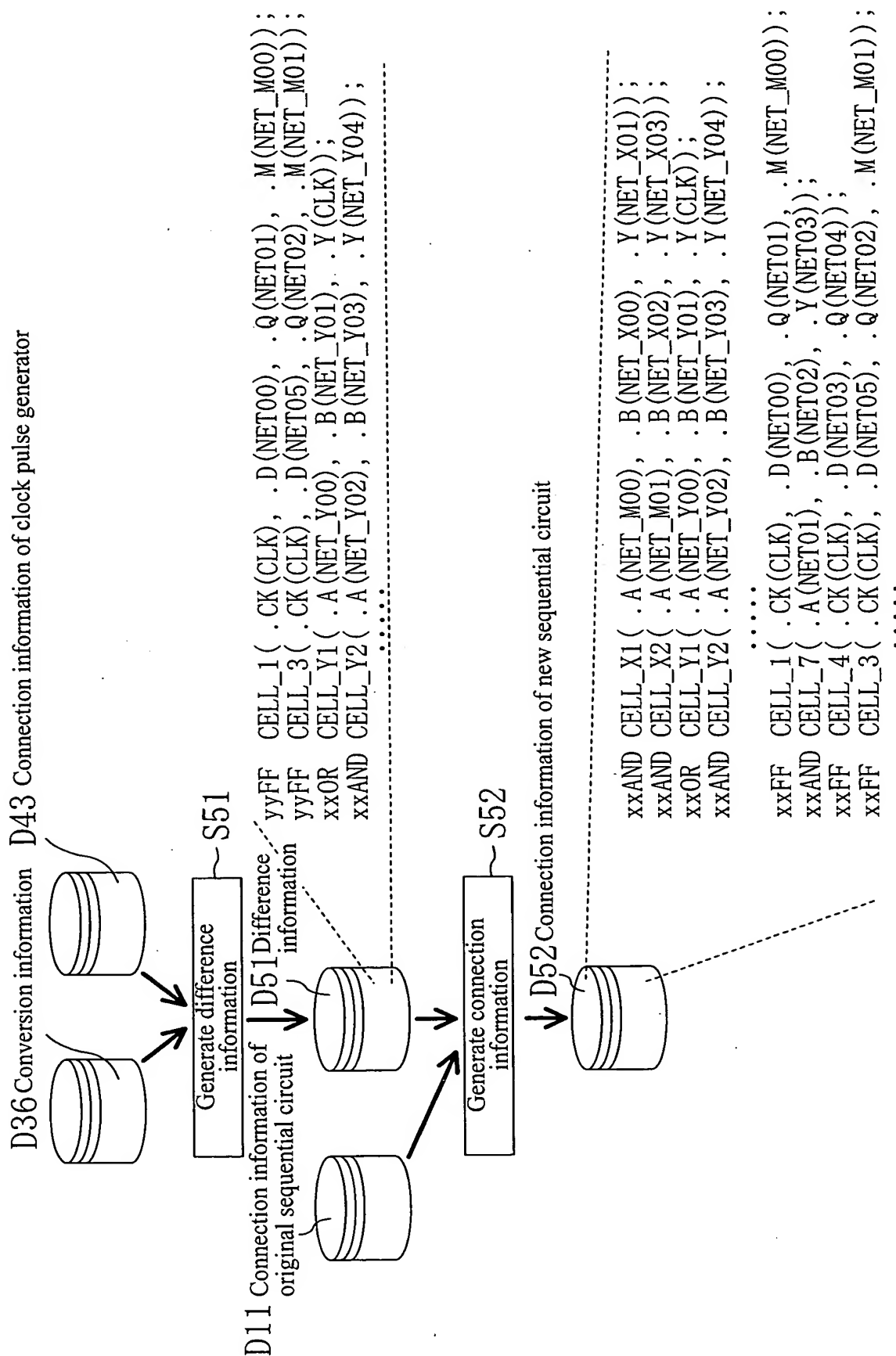


FIG. 27

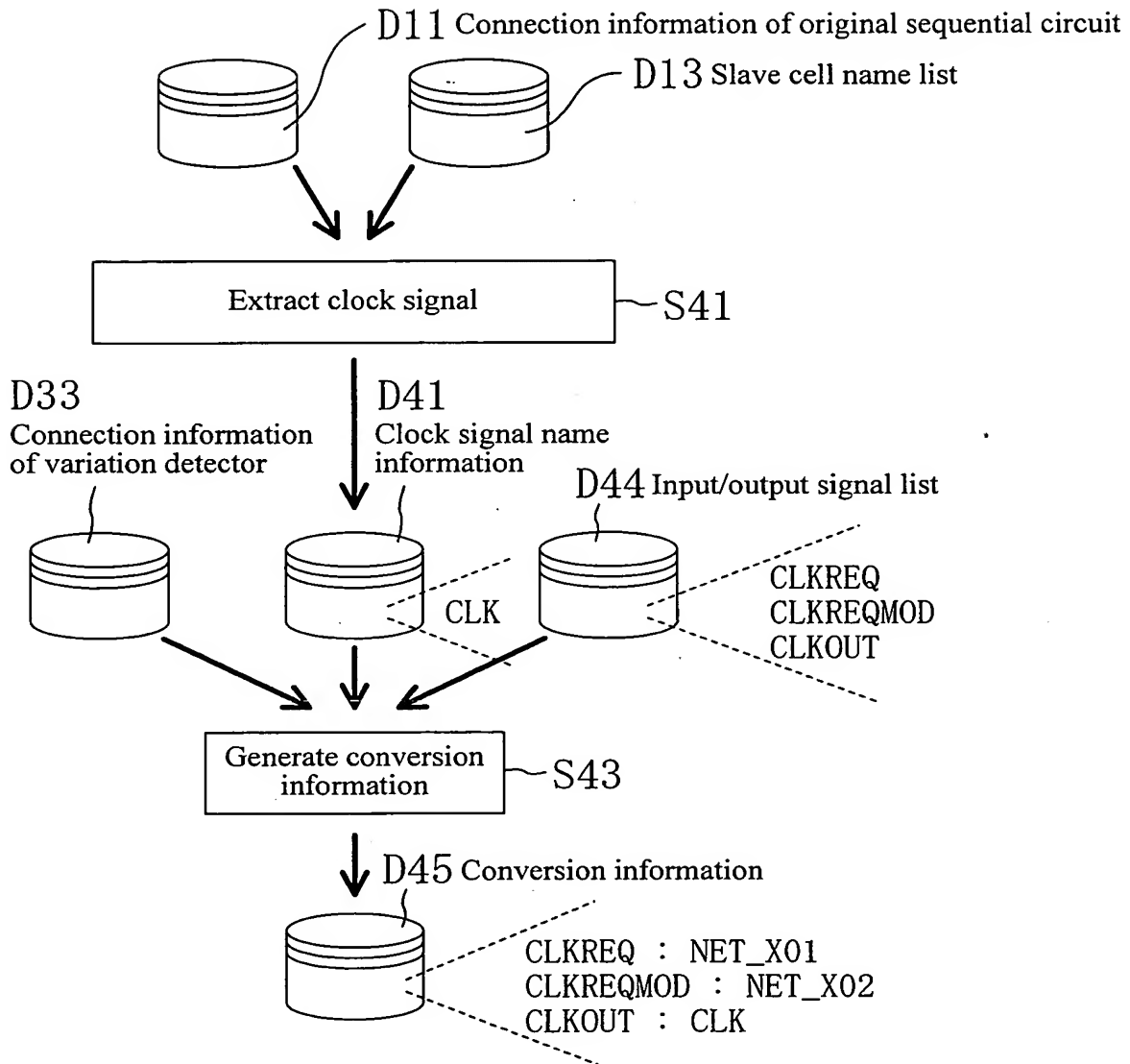


FIG. 28

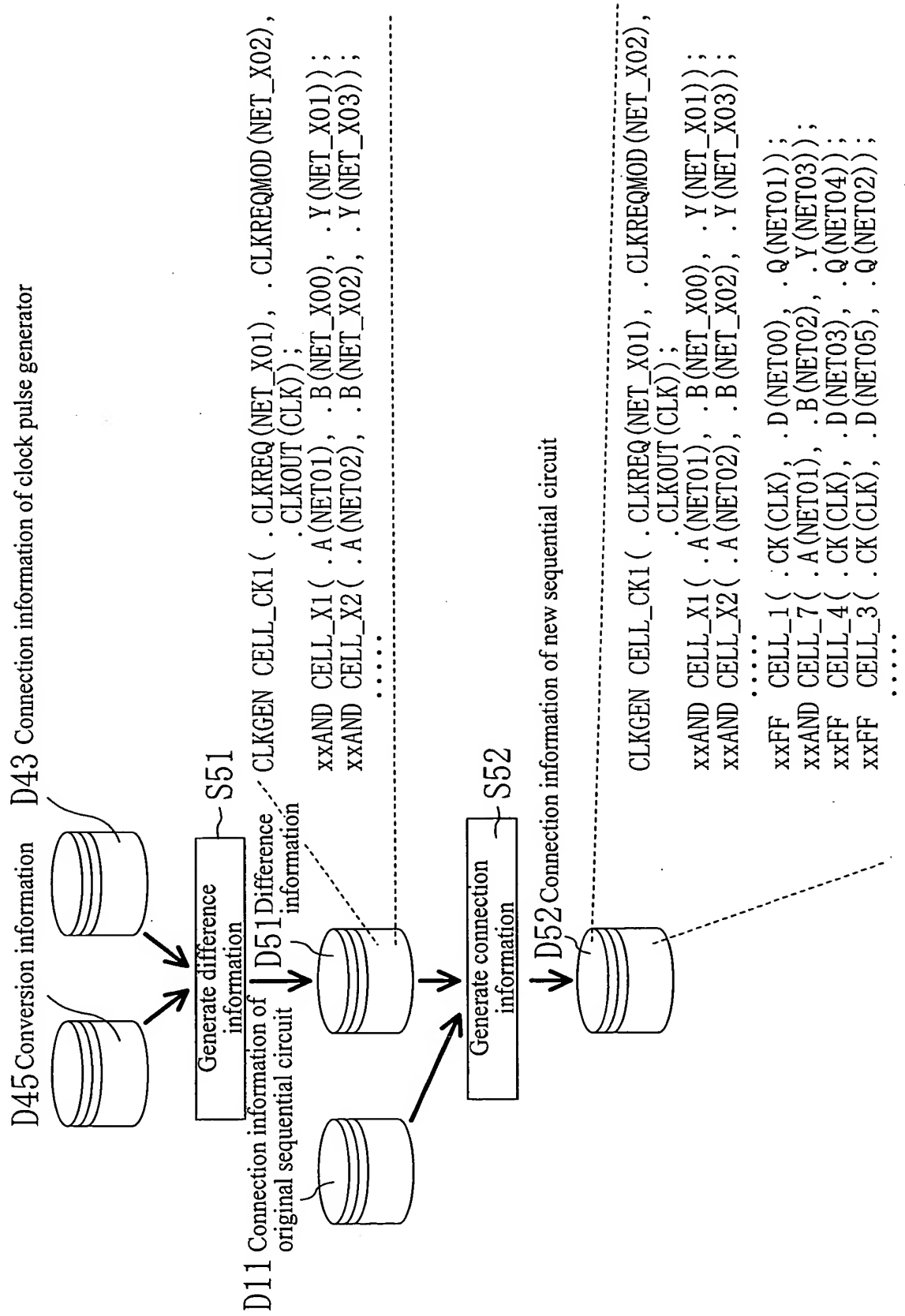


FIG. 29

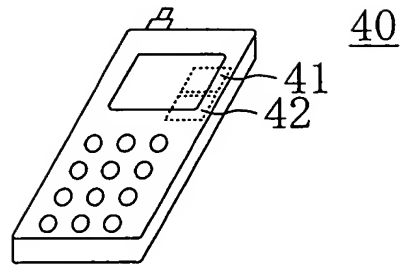


FIG. 30

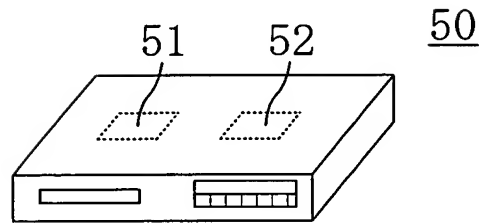


FIG. 31

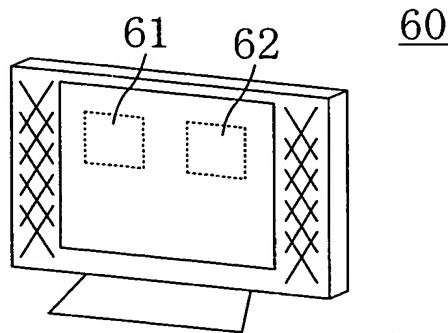


FIG. 32

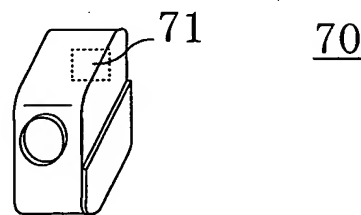


FIG. 33

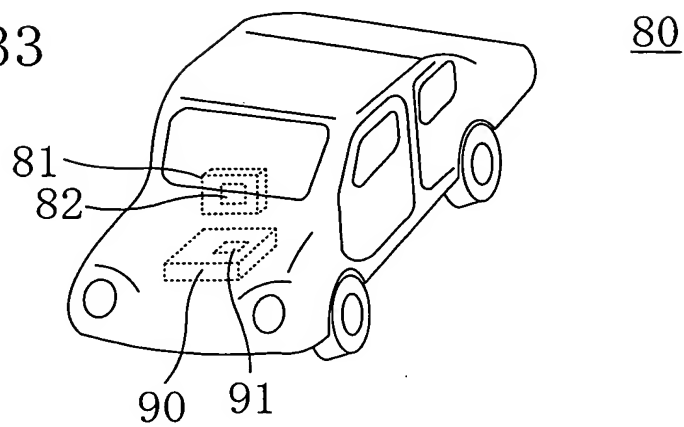
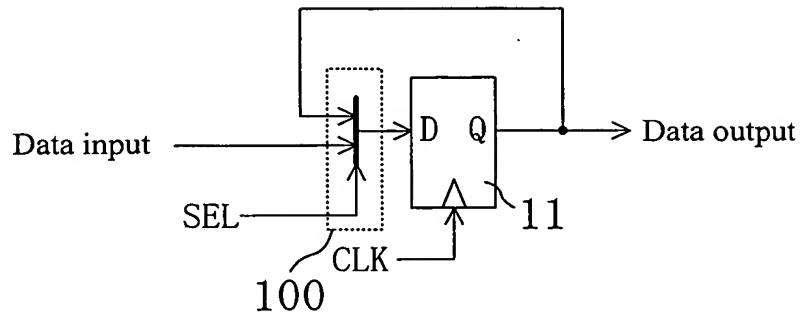


FIG. 34A



Replaceable because updating
function of memory element is
considered to be equivalent.

FIG. 34B

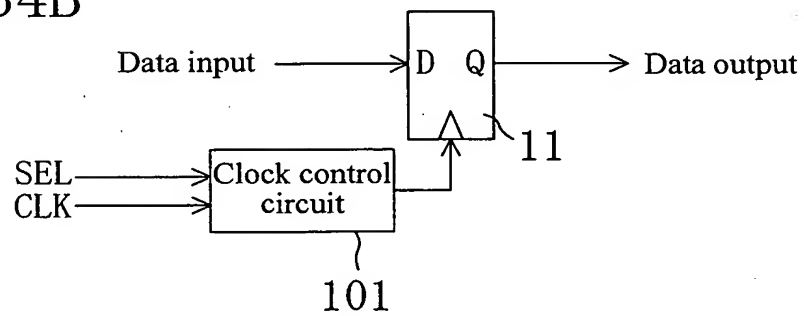
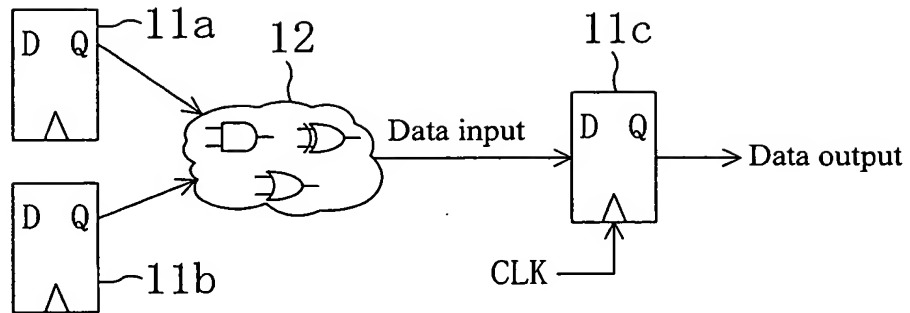


FIG. 35A



Replaceable because of being equivalent even if CLK is stopped so long as variation in output of memory element does not affect functions

FIG. 35B

